# Guru Nanak Dev Engineering College, Ludhiana Department of Computer Science & Engineering

Ref. No.: 43E 229

Dated: .. 29 2. 2018

Minutes of meeting of internal members of Board of Studies of Computer Science and Engineering department held on 28.02.2018 at 2:00pm in the office of HOD (CSE).

The following persons were present:

- 1. Dr. Parminder Singh, Professor & Head, Department of Computer Science and Engineering, GNDEC, Ludhiana (Chairman)
- 2. Er. Amanpreet Singh Brar, Associate Professor, Department of Computer Science and Engineering, GNDEC, Ludhiana (Member)
- 3. Er. Amandeep Kaur Sohal, Assistant Professor, Department of Computer Science and Engineering, GNDEC, Ludhiana (Member)
- 4. Er. Sumeet Kaur Sehra, Assistant Professor, Department of Computer Science and Engineering, GNDEC, Ludhiana (Member)
- 5. Er. Vivek Thapar, Assistant Professor, Department of Computer Science and Engineering, GNDEC, Ludhiana (Member)

The meeting started with welcome address by chairman BOS. Following decisions were taken unanimously:

## Proceedings:

## Agenda Item 1

To discuss and finalise the unit wise contact hours for M.Tech. (Computer Science and Engineering) 2014 scheme.

Contact hours are mentioned for each unit (section) of the syllabus of subjects of M.Tech. (Computer Science and Engineering) 2014 scheme. Updated syllabus attached as Annexure-I.

# <u>Agenda Item 2</u>

Change of subject codes of B.Tech. (Computer Science and Engineering) 2017 scheme.

Subject codes are changed and are as following:

Dr. Parminder Singh

Er. Antanpreet Singh Brar

Er. Amandeep Kaur Sohal

Er. Sumeet Kaur Sehra

Course	Course Name	Science and Engine  Load Allocation				rse Scheme		T
Code		L	T	P	Internal	External	Total Marks	Credits
<del></del>		_ <u></u>	<u> </u>	emester	<u> </u>	LOATER MAP	<u>                                      </u>	<u> </u>
CS-17301	Mathematics-III	3		СШЕЗІСІ		T	· · · · · · · · · · · · · · · · · · ·	
	Transferrance, III	3	1	-	40	60	100	4
CS-17302	Computer Architecture and Organization	3	1	•	40	60	100	4
CS-17303	Digital Circuits and Logic Design	3	ì	-	40	60	100	4
CS-17304	Data Structures and Algorithms	3	l	-	40	60	100	4
CS-17305	Object Oriented Programming using C++	3	1	-	40	60	100	4
CS-17306	Digital Circuits and Logic Design Lab	-	_	4	30	20	50	2
CS-17307	Data Structures and Algorithms Lab	•	-	4	30	20	50	2
CS-17308	Object Oriented Programming using C++ Lab	•	-	4	30	20	50	2
TR-17301	Workshop Training*	<b></b>			60	40	100	2
Total		15	5	12	350	400	750	28
		Conts	ct Hou	rs= 32				
		F	ourth S	emester		<del></del>		<u> </u>
CS-17401	Discrete Structures	3	1	•	40	60	100	4
CS-17402	Operating System	3	1	•	40	60	100	4
CS-17403	Computer Networks	3	1	-	40	60	100	4
CS-17404	Microprocessor Architecture and Programming	3	1	-	40	60	100	4
CS-17405	Java Programming	3	1	-	40	60	100	4
CS-17406	Operating Systems Lab	-	-	4	30	20	50	2
CS-17407	Computer Networks Lab	<del>  -  </del>		4	30	20	50	2

Dr. Parminder Singh

Er. Sumeet Kaur Sehra

Er. Amanpreet Singh Brar

Er. Amandeep Kaur Sohal

CS-17408	Microprocessor	<del>                                     </del>	Τ	2	30		<del>, </del>	<del>-</del>
	Architecture and Programming Lab				30	20	50	1
CS-17409	Java Programming Lab	-	-	4	30	20	50	2
GF-17401	General Fitness	<del> </del>	<del>  -</del> -	<del>  -                                   </del>	100	<del>-</del>	100	 
<b>Fotal</b>		15	5	14	420	380	800	28
		Cont	act Hou	ırs= 34				20
		<del>-1</del> j	Fifth Se	mester	· <del>-</del> -	<del>-</del> · · <u>- · · · · · · · · · · · · · · · · </u>		<del></del> ,
CS-17501	Relational Database Management System	3	1	T -	40	60	100	4
CS-17502	Computer Graphics	3	1	-	40	60	100	4
CS-17503	Design and Analysis of Algorithms	3	1	-	40	60	100	4
CS-17504	Web Technologies	3	1	† -	40	60	100	4
DECS-175xx	Elective-I	3	1	+ -	40	60	100	4
CS-17511	Relational Database Management System Lab	-	-	4	30	20	50	2
CS-17512	Computer Graphics Lab	_	-	2	30	20	50	i
CS-17513	Design and Analysis of Algorithms Lab	-	-	2	30	20	50	1
CS-17514	Web Technologies Lab	-	-	4	30	20	50	2
R-17501	Industrial Training-I*	<u> </u>	<u> </u>	<u></u>	60	40	100	2
l otal		15	5	12	380	420	800	28
		Contact Hours 32		s= 32				
		Si	ixth Se	mester			L	<u> </u>
CS-17601	Theory of Computation	3	1	- 7	40	60	100	4
CS-17602	Advanced Database Systems	3	1	-	40	60	100	4
S-17603	Software Engineering	3	1	-	40	60	100	4
DECS-176xx	Elective-II	3	<u> </u>	┝╌╌┤	40	60	100	4

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Er. Amandeep Kaur Sohal

Er. Sumeet Kaur Sehra

OECS-176xx	Open Elective	3	T -		40	60	100	3
CS-17610	41			<del>-</del>				]
	Advanced Database Systems Lab	•	-	4	30	20	50	2
CS-17611	Software Engineering Lab	-	•	4	30	20	50	2
DECS-176xx	Elective-II Lab	•	-	2	30	20	50	1
PRCS-17601	Minor Project	-	-	1	60	40	100	1
GF-17601	General Fitness	-	-	-	100		100	1
Total		15	4	11	450	400	850	26
		Conta	ct Hours	= 30				
		Sevent	h/Eight	h Seme	ster			
CS-17701	Advanced Computer Networks	3	1	-	40	60	100	4
CS-17702	Compiler Design	3	1	1 -	40	60	100	4
CS-17703	Cyber Laws and IPR	3	1	-	40	60	100	4
DECS-177xx	Elective-III	3	1	<del>  -</del>	40	60	100	4
DECS-177xx	Elective-IV	3	1	-	40	60	100	4
CS-17715	Advanced Computer Networks Lab	-	-	4	30	20	50	2
PRCS-17701	Major Project	•	-	3	120	80	200	3
DECS-177xx	Elective-III Lab	•	-	2	30	20	50	1
3F-17701	General Fitness	-	-	1 -	100	-	100	1
Total		15	5	09	480	420	900	27
<u> </u>	<del> </del>	Contac	et Hours	<b>= 29</b>				ļ
		Sevent	h/Eightl	Semes	ter	<u> </u>		<del></del>
TRCS-17701	Industrial Training-II*		-		450	350	800	13
TRCS-17702	Industry Oriented Training <sup>3</sup>		•		200	-	200	02
Total					650	350	1000	15

\*Workshop training will be imparted at the end of 2 semester in the institute for four weeks.

<sup>26</sup>06 weeks Industrial Training-I will be after the end of 4th semester.

\*This component will be based on Industrial Training/Institute Department Training/Project Work/Skill Development (Grade-6) Entrepreneurship Training facility created by IKG-PTU, Mohali.

This component will be based on Two Weeks Workshop/Software/Latest Development through Global Initiative of Academic Networks (GIAN).

Dr. Parminder Singh

Er. Amanpreet Singh Brar

Er. Amandeep Kaur Sohal

Er. Sumeer Kaur Sehra

# List of Electives

#### Elective-I

**DECS-17505 Advanced Computer Architecture** 

DECS-17506 Cryptography and Network Security

**DECS-17507 Wireless Networks** 

**DECS-17508 Ethical Hacking** 

**DECS-17509 Network Protocols** 

DECS-17510 Embedded Systems

#### **Elective-II**

DECS-17604 Simulation and Modelling

**DECS-17605 Digital Image Processing** 

DECS-17606 Artificial Intelligence

DECS-17607 Advanced Java

DECS-17608 .NET Technologies

**DECS-17609 Linux Administration** 

DECS-17613 Simulation and Modelling Lab

DECS-17614 Digital Image Processing Lab

DECS-17615 Artificial Intelligence Lab

DECS-17616 Advanced Java Lab

DECS-17617 .NET Technologies Lab

DECS-17618 Linux Administration Lab

#### **Elective-III**

**DECS-17704 Parallel Computing** 

**DECS-17705 Mobile Computing** 

DECS-17706 Cloud Computing

DECS-17707 Big Data and Business Analytics

DECS-17708 Data Warehouse and Data Mining

DECS-17717 Parallel Computing Lab

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DECS-17718 Mobile Computing Lab

**DECS-17719 Cloud Computing Lab** 

DECS-17720 Big Data and Business Analytics Lab

DECS-17721 Data Warehouse and Data Mining Lab

#### **Elective-IV**

DECS-17709 Natural Language Processing

**DECS-17710 Machine Learning** 

DECS-17711 Software Testing and Quality Assurance

**DECS-17712 Information Security** 

**DECS-17713 Soft Computing** 

DECS-17714 Agile Software Development

## **Open Electives**

OECS-17601 Software Project Management

OECE-17602 Object Oriented Programming using JAVA

#### Agenda Item 3

Discussion about subject to be taught to first year students of all B.Tech. branches for basics knowledge of computer system and programming in new 2018 scheme of B.Tech. for all branches.

It is decided that subject titled "Programming for Problem Solving" (Theory and Laboratory) to be taught to first year students of all branches as per following details:

Subject Code	Course Name	Load	Credits		
		L	T	P	<b>†</b> ∙
ESC-18105	Programming for Problem Solving	3	1		4
ESC-18106`	Programming for Problem Solving Laboratory	<del> </del>	-	2	<del> </del>

Detailed syllabus of Theory and Laboratory of subject "Programming for Problem Solving" is attached as Annexure-II.

Dr. Parmender Singh

Er. Aman preet Singh Brar

Er. Amandeep Kaur Sohal

Er. Sumeet Kaur Sehra

#### Agenda Item 4

To revise the syllabus of Mathematics-III (CS-17301) for 3<sup>rd</sup> semester B.Tech. (Computer Science and Engineering) scheme 2017.

The revised syllabus of Mathematics-III (CS-17301) for 3<sup>rd</sup> semester B.Tech. (Computer Science and Engineering) scheme 2017 is approved as recommended by the concerned committee of Applied Science department. The revised syllabus is attached as Annexure-III.

Dr. Parminder Singh

Er. Amannetet Singh Brar

Er. Amandeep Kaur Sohal

Er. Sume Kaur Sehra