# Guru Nanak Dev Engineering College, Ludhiana Department of Computer Science & Engineering

Rq. No.: (SE/03).

Dated: 9-6-15

Minutes of 4th meeting of Board of Studies of Computer Science and Engineering department held on 08.06.2015 at 11:00 am in the college committee room.

The following persons were present:

- 1. Dr. Parminder Singh, Associate Professor& Head, Department of Computer Science and Engineering, GNDEC, Ludhiana (Chairman)
- 2. Er. Amanpreet Singh Brar, Associate Professor, Department of Computer Science and Engineering, GNDEC, Ludhiana (Member)
- 3. Er. Sumeet Kaur Sehra, Assistant Professor, Department of Computer Science and Engineering, GNDEC, Ludhiana (Member)
- 4. Er. Vivek Thapar, Assistant Professor, Department of Computer Science and Engineering, GNDEC, Ludhiana (Member)
- 5. Dr. Gurpreet Singh Lehal, Professor, Department of Computer Science, Punjabi University, Patiala (Member)
- 6. Dr. Sukhwinder Singh, Professor, UIET, Panjab University Chandigarh (Member)
- 7. Dr. Paramjit Singh, Associate Professor, Giani Zail Singh Punjab Technical University Campus, Bathinda (Member)
- 8. Er. Nitish Mahajan, MD, Conjoinix Technology Private Limited, Sector 34-A, Chandigarh (Member)

The meeting started with welcome address by chairman BOS, and the already circulated agenda was put up for discussion. Following decisions were taken unanimously:

# Proceedings:

# Agenda Item 1

# 1.1 Finalizing the new scheme for B.Tech. Computer Science and Engineering

#### Approved

After discussion and deliberation, necessary modifications have been incorporated in the proposed scheme of B.Tech. Computer Science and Engineering.

Committee approved the scheme and authorized Chairman BOS for further changes if required.

Dr. Parminder Singh

Er. Amanpreet Singh Brar

Er. Sumeet Kaur Sehra

Er. VivekThapar W

Dr. Gurpreet Singh Lehal

Dr. Sukhwinder Singhe

Dr. Paramjit Singh

Course	Course Name	Load Allocation			Marks Distribution		Total	Credits
Code		L	T	P	Internal	External	Marks	
BTAM-301	Mathematics-III	3	1	-	40	60	100	4
BTCSE-301	Computer Architecture and Organisation	3	1		40	60	100	4
BTCSE-302	Digital Circuits and Logic Design	3	1	-	40	60	100	4
BTCSE-303	Data Structures and Algorithms	4	1	-	40	60	100	5
BTCSE-304	Object Oriented Programming using C++	4	1	_	40	60	100	5
B ICSE-305	Digital Circuits and Logic Design Lab	-	-	2	30	20	50	1
BTCSE-306	Data Structures and Algorithms Lab	-	-	4	30	20	50	2
BTCSE-307	Object Oriented Programming using C++ Lab	-	-	4	30	20	50	2
BTCSE-308	Institutional Practical Training*				60	40	100	1
Total		17	5	10	350	400	750	28
		Contact Hours= 32			1			

<sup>\*</sup>Institutional training will be imparted at the end of 2<sup>nd</sup> semester in the institute for four weeks

Er. Amanpreet Singh Brar
Er. Sumeet Kaur Sehra

Er. VivekThapar

Dr. Gurpreet Singh Lehal

Dr. Sukhwinder Singh

Dr. Paramjit Singh

Course Code	Course Name	Load Allocation			Marks Distribution		Total	Credits
		L	T	P	Internal	External	Marks	
BTCSE-401	Discrete Structures	3	1	-	40	60	100	4
BTCSE-402	Operating System	3	ì	-	40	60	100	4
BTCSE-403	Computer Networks	3	1	-	40	60	100	4
BTCSE-404	Microprocessor Architecture and Programming	3	1	-	40	60	100	4
BTCSE-405	Java Programming	4	1	-	40	60	100	5
TCSE-406	Operating Systems Lab	-	<u> </u>	2	30	20	50	1
BTCSE-407	Computer Networks Lab	_	-	4	30	20	50	2
BTCSE-408	Microprocessor Architecture and Programming Lab	-	_	2	30	20	50	1
BTCSE-409	Java Programming Lab	-	-	4	30	20	50	2
General Fitn	ess	1		<u>l .</u>	100	-	100	-
Total		16	5	12	420	380	800	27
		Cont	act Ho	urs= 33	1			

Er. Amanpreet Singh Bran

Er. Sumeet Kaur Sehra

Er. VivekThapar

Dr. Gurpreet Singh Lehal

Dr. Sukhwinder Singh 🕟

Dr. Paramjit Singh

Course	Course Name	Load Allocation			Marks Distribution		Total	Credits
Code		L	T	P	Internal	External	Marks	
BTCSE-501	Relational Database Management System	3	1	-	40	60	100	4
BTCSE-502	Computer Graphics	3	1	-	40	60	100	4
BTCSE-503	Design and Analysis of Algorithms	3	1	-	40	60	100	4
BTCSE-504	Web Technologies	3	1	1	40	60	100	4
BTCSE-505	Cyber Laws and IPR	3	1	-	40	60	100	4
TCSE-506	Relational Database Management System Lab	-	-	4	30	20	50	2
BTCSE-507	Computer Graphics Lab	-		2	30	20	50	1
BTCSE-508	Design and Analysis of Algorithms Lab	-	-	2	30	20	50	1
BTCSE-509	Web Technologies Lab	-	-	4	30	20	50	2
BTCSE-510	Industrial Training	<del> </del>		<del>                                     </del>	60	40	100	2
Total		15	5	12	380	420	800	28

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Er. Sumeet Kaur Sehr

Er. VivekThapar

Dr. Gurpreet Singh Lehal

Dr. Paramjit Singh

SIXTH SE	Course Name	Load Allocation			Marks Distribution		Total Marks	Credits
Code		L	T	P	Internal	External	WIAI NO	
BTCSE-601	Theory of Computation	3	1	-	40	60	100	4
BTCSE-602	Advanced Database	4	1		40	60	100	5
BTCSE-603	Systems Software Engineering	3	1		40	60	100	4
BTCSE-xxx	Elective-I	3	1		40	60	100	4
BTCSE-xxx	Elective-II	3	1	-	40	60	100	4
BTCSE-604	Advanced Database	-		4	30	20	50	2
BTCSE-605	Systems Lab Software Engineering	<del>  -</del>	-	4	30	20	50	2
BTCSE-xxx	Lab	<del> </del> -	-	4	30	20	50	2
	General Fitness	<u> </u>			100	<del> </del>	100	
Total		16	5	12	390	360	750	27
TOM	Contact Hours= 33			1		Ì		

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Er. VivekThapar W

Dr. Gurpreet Singh Lehal Nor. Sukhwinder Singh

Dr. Paramjit Singh

15

Advanced Computer

General Fitness

Networks Lab

Project

1

TCSE-703

BTCSE-704

Total

SEVENTE	<del>I/EIGHTH SEMES</del>	TER						<del>,</del>
Course Name Code						Marks Distribution		Credits
		L	T	P	Internal	External		
BTCSE-801	Industrial Training	-	_	- -	450	300	750	20

Contact Hours= 31

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Dr. Sukhwinder Singh

Dr. Paramjit Singh

Total

Marks

100

100

100

100

100

50

200

100

850

100

420

100

100

430

10

12

Credits

4

4

3

1

5

25

# 1.2 Finalizing the elective subjects for new scheme for B.Tech. Computer Science and Engineering

# Approved

#### **Elective-I:**

BTCSE-606Simulation and Modeling

BTCSE-607Digital Image Processing

BTCSE-608Artificial Intelligence

BTCSE-609Advanced Java

BTCSE-610.NET Technologies

BTCSE-611Linux Administration

BTCSE-612 Simulation and Modeling Lab

BTCSE-613 Digital Image Processing Lab

BTCSE-614 Artificial Intelligence Lab

BTCSE-615 Advanced Java Lab

BTCSE-616 .NET Technologies Lab

BTCSE-617 Linux Administration Lab

#### **Elective-II:**

BTCSE-618Advanced Computer Architecture

BTCSE-619Cryptography and Network Security

BTCSE-620Wireless Networks

BTCSE-621Ethical Hacking

**BTCSE-622Network Protocols** 

BTCSE-623Embedded Systems

#### **Elective-III:**

**BTCSE-705 Parallel Computing** 

**BTCSE-706 Mobile Computing** 

**BTCSE-707 Cloud Computing** 

BTCSE-709 Big Data and Business Analytics

BTCSE-710 Data Warehouse and Data Mining

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#### Elective-IV:

**BTCSE-711Natural Language Processing** 

**BTCSE-712Machine Learning** 

BTCSE-713Software Testing and Quality Assurance

BTCSE-714Information Security

**BTCSE-715Soft Computing** 

BTCSE-716Agile Software Development

#### **Open Electives:**

BTCSE-717 Operation Research

BTCSE-718 Engineering Economics and Management

**BTCSE-719 Organizational Structures** 

BTCSE-720 Project management

BTCSE-721 Optimization Techniques

BTCSE-722 Human Resource Management

BTCSE-723 Industrial Pshycology

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# 1.3 Finalizing the syllabus of subjects of 3<sup>rd</sup> and 4<sup>th</sup> semester of B.Tech. Computer Science and Engineering new scheme.

# Approved

After discussion and deliberation, certain modifications have been done and others recommendations will be incorporated in the proposed Syllabi of subjects of B.Tech. Computer Science and Engineering.

Committee approved the syllabus and authorized Chairman BOS for further changes if required.

THIRD SEMESTER						
Course Code	Course Name	Suggestions				
BTAM-301	Mathematics-III	No. of hours to be revised.				
BTCSE-301	Computer Architecture and Organisation	<ul> <li>No. of hours for control unit to be revised.</li> <li>Data representation chapter should be revised.</li> </ul>				
BTCSE-302	Digital Circuits and Logic Design	Book of Anant Kumar has to be added     Family class topic				
BTCSE-303	Data Structures and Algorithms	No. of Lectures per unit to be revised.				
BTCSE-304	Object Oriented Programming using C++	No. of Lectures per unit to be revised.				
BTCSE-305	Digital Circuits and Logic Design Lab					
BTCSE-306	Data Structures and Algorithms Lab	<ul> <li>Data structure lab programs should be application oriented</li> <li>Sorting and searching programs should be included in as single program and compare these algorithms on different types of inputs.</li> </ul>				
BTCSE-307	Object Oriented Programming using C++ Lab					
BTCSE-308	Institutional Practical Training*	Institutional practical training should be diversify by including some vocational courses.				

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Er. VivekThapar

Dr. Paramjit Singh

Dr. Gurpreet Singh Lehal X

Dr. Sukhwinder Singh

FOURTH S	SEMESTER	
Course Code	Course Name	Suggestions
BTCSE-401	Discrete Structures	Book of C.L. Liu to be included     Kruskal's algorithm (minimum spanning tree)
BTCSE-402	Operating System	Book of Godbole to be included
BTCSE-403	Computer Networks	Lecture distribution should be included
BTCSE-404	Microprocessor Architecture and Programming	<ul> <li>Chapter of microcontroller to be removed.</li> <li>Chapter on interfacing applications should be included</li> <li>Book of A.P. Mathur and Sridhar and B. Ram to be included.</li> </ul>
BTCSE-405	Java Programming	
BTCSE-406	Operating Systems Lab	Syllabus should be revised according to 2 Hr lab and focus should be on shell programming.
BTCSE-407	Computer Networks Lab	<ul> <li>Configuration of DHCP</li> <li>Proxy settings</li> </ul>
BTCSE-408	Microprocessor Architecture and Programming Lab	Syllabus should be revised by removing microcontroller practicals and including interfacing applications.
BTCSE-409	Java Programming Lab	

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### Agenda Item 2

Approval of syllabus of subjects for new scheme of M.Tech. Computer Science and Engineering, after incorporation of suggestions given in 3rd BOS meeting held on 6th Feb., 2015.

Approved

### Agenda Item 3

Approval of external question paper setters for end semester examinations of B.Tech. and M.Tech.

Approved

# General Suggestions:

Internal evaluation of project work will include a seminar that is to be presented by each student.

• Project evaluation should be carried out by the industry expert and preferably one expert for about 10 students.

• To enhance industry institute interaction, regular expert talks should be carried by the experts from industry.

• Some provision should be made at institute level to include non credit course for teaching certain foreign languages.

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